USN

Third Semester B.E. Degree Examination, June/July 2014 Electronic Instrumentation

Time: 3 hrs. Max. Marks: 100

Note: Answer any FIVE full questions, selecting atleast TWO questions from each part.

PART - A

1	a. b.	Explain with a neat block diagram of TRUE RMS voltmeters: Convert a basic D'Arsonal movement with an internal resistance of 50Ω and deflection current of 2mA into a multirange dc voltmeter with voltage range of $0-50V$, $0-100V$ and $0-250V$. Connect the multiplier resistances in D'Arsonal movement.	f 0 - 10V,
2	a. b.	With a neat block diagram, explain the successive approximation DVM. With a neat block diagram, explain the digital frequency meter.	(10 Marks) (10 Marks)
3	a. b.	With a neat block diagram, explain the general purpose CRO. With a neat block diagram, explain the typical CRT connections.	(10 Marks) (10 Marks)
4	a. b.	With a neat block diagram, explain the digital storage oscilloscope. With a neat block diagram, explain the sampling oscilloscope.	(10 Marks) (10 Marks)
PART – B			
5	a. b.	With a neat block diagram, explain the working principle of pulse generator. With a neat block diagram, explain the working principle of function generator.	(10 Marks) (10 Marks)
6	a. b.	With a neat block diagram, explain the Wein's bridge to measure the frequency. With a neat block diagram, explain the Wagner's earth connections.	(10 Marks) (10 Marks)
7	a. b.	Explain the construction and working of LVDT. Explain the construction and working of thermistor. What are the salient features of the construction and working of the construction and the construction an	(10 Marks) of it. (10 Marks)
8	a. b. c.	Explain the following with relevant sketch: Photo electric transducer. Piezo electro transducer. RTD.	(07 Marks) (06 Marks) (07 Marks)

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